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Project Demos are scheduled for: 4:45-6:00pm on Monday 6 December and Wednesday 8 December in the VLSI Design Lab (sign up for a specific time on the signup sheet on the door of the lab) Students are expected to attend all project demos unless they discuss any necessary absences with Prof. Walker in advance of their absence Project Reports are due to Prof. Walker by 3:00pm on Thursday 9 December ...

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